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FORM**

(to be used for all correspondence after initial filing)

Application Number 09/990,060

Filing Date November 21, 2001

First Named Inventor Gredone et al.

Art Unit 2116

Examiner Name Nitin C. Patel

Attorney Docket Number I-2-0201US

Total Number of Pages in This Submission

ENCLOSURES (Check all that apply)

- | | | |
|---|---|---|
| <input type="checkbox"/> Fee Transmittal Form | <input type="checkbox"/> Drawing(s) | <input type="checkbox"/> After Allowance communication to Technology Center (TC) |
| <input type="checkbox"/> Fee Attached | <input type="checkbox"/> Licensing-related Papers | <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences |
| <input type="checkbox"/> Amendment/Reply | <input type="checkbox"/> Petition | <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) |
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| <input checked="" type="checkbox"/> Supplemental Information Disclosure Statement | <input type="checkbox"/> CD, Number of CD(s) _____ | |
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Louis Weinstein	Reg. No. 20,477
	Volpe and Koenig, P.C.	
Signature		
Date	February 4, 2005	

CERTIFICATE OF TRANSMISSION/MAILING

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the **PATENT APPLICATION** of:

Gredone et al.

Application No.: 09/990,060

Confirmation No.: 1472

Filed: November 21, 2001

For: HYBRID PARALLEL/SERIAL BUS
INTERFACE

Group: 2116

Examiner: Nitin C. Patel

Our File: I-2-201US

Date: February 4, 2005

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Further to Applicants' Duty of Disclosure pursuant to 37 C.F.R. §1.56, Applicants wish to bring to the Examiner's attention the newly cited material on the enclosed PTO-1449 form. Newly cited documents are indicated by an asterisk (*). Copies of the cited documents are enclosed.

Pursuant to 37 C.F.R. § 1.97(e)(1), the undersigned certifies that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application, copy enclosed,

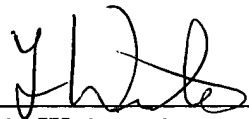
Applicant: Gredone et al.
Application No.: 09/990,060

and that this communication was not received by any individual designated in §1.56(c) more than three (3) months prior to the filing of the information disclosure statement.

It is respectfully requested that the Examiner consider these documents and return an initialed copy of the PTO-1449 form indicating his consideration of the cited materials.

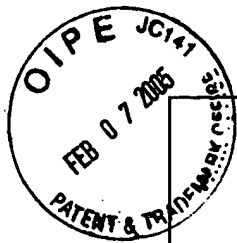
Respectfully submitted,

Gredone et al.

By 
Louis Weinstein
Registration No. 20,477
(215) 568-6400

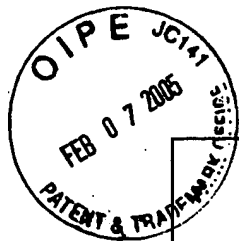
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Enclosures (6)



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				APPLICANT Gredone et al.			
				FILING DATE November 21, 2001		GROUP 2116	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		4,675,861	06/1987	Uttermark			
		5,018,142	05/1991	Simcoe et al.			
		5,136,587	08/1992	Obana et al.			
		5,327,126	07/1994	Beanland			
		5,400,369	03/1995	Ikemura			
		5,526,360	06/1996	Kraft			
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		5,602,780	02/1997	Diem et al.			
		5,847,578	12/1998	Noakes et al.			
		5,926,120	07/1999	Swenson et al.			
		6,040,792	03/2000	Watson et al.			
		6,122,683	09/2000	Ku et al.			
		6,128,244	10/2000	Thompson et al.			
		6,134,247	10/2000	Solana de Quesada et al.			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
		42847	04/1991	KR			
		1997-56528	07/1997	KR			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
	*	"21-Bit Channel Link", National Semiconductor, July 1997, http://www.national.com/ds/DS/DS90CR21.pdf					
	*	Novak et al., "Channel Link Moving and Shaping Information in Point-To-Point Application", National Semiconductor, May 1996, http://www.national.com/an/AN/AN-1041.pdf					
EXAMINER				DATE CONSIDERED			

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		APPLICANT Gredone et al.	
		FILING DATE November 21, 2001	GROUP 2116
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)			
	*	von Herzen et al., "Multi-Chanel 622 Mb/s LVDS Data Transfer for Virtex-E Devices", Xilinx Inc., January 6, 2001, http://direct.xilinx.com/bvdocs/appnotes/xapp233.pdf .	
	*	Kitanovska et al., "Bus LVDS with Virtex-E Devices", Xilinx Inc., July 26, 2000, http://direct.xilinx.com/bvdocs/appnotes/xapp243.pdf .	

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